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Interleaving & deinterleaving apparatus/SAMSUNG, European Patent Office (EPO Board of Appeal), 30 April 2009

Ferry van Looijengoed · Thursday, April 30th, 2009 · Landmark European Patent Cases

Claim 1 of the patent application contained the feature that ‘the device is adapted to generate L addresses, which are smaller in number than $N = N_g \times 2m^2$ virtual addresses for reading data from said interleaver memory in which L data bits are stored’. The Board of Appeal noted that it might be true that claim 1 did not imply anything about optimal choices of m and N_g . However, according to the present Board, it is not a requirement of the European Patent Convention, and in particular not of Article 84 EPC, that the claims should specify the optimum way of carrying out the invention.

A [full summary](#) of this case has been published on [Kluwer IP Law](#).

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